

**IN THE CLAIMS:**

1. (Currently Amended) An apparatus in a receiver that is configured for receiving~~For use with a scalable video decoder capable of and~~ decoding an incoming scalable video bit stream that has been encoded by a transmitter for transmission externally from the transmitter and further configured for generating a baseband video signal, an~~said~~ apparatus being configured for controlling a processing load of said scalable video decoder, said apparatus comprising:

an analyzer circuit ~~capable of~~configured for measuring, from the received bit stream, at least one characteristic of said ~~incoming scalable video~~received bit stream and generating at least one video parameter associated with said at least one characteristic; and

a processor load controller ~~capable of~~configured for receiving said at least one video parameter and, in response ~~thereto~~, controlling a level of decoding of said ~~incoming scalable video~~received bit stream performed by said scalable video decoder.

2. (Original) The apparatus as set forth in Claim 1 wherein said at least one video parameter indicates a level of motion of frames in said incoming scalable video bit stream.

3. (Currently Amended) The apparatus as set forth in Claim 1 wherein said ~~wherein said~~ at least one video parameter indicates a level of detail of frames in said incoming scalable video bit stream.

4. (Currently Amended) The apparatus as set forth in Claim 1 wherein said processor load controller is further ~~capable of~~configured for receiving a frame type parameter associated with a first frame in said incoming scalable video bit stream.

5. (Original) The apparatus as set forth in Claim 4 wherein said frame type parameter comprises at least one of an I-frame parameter, a B-frame parameter, and a P-frame parameter.

6. (Currently Amended) The apparatus as set forth in Claim 5 wherein said processor load controller is further ~~capable of~~configured for receiving a source type parameter associated with said first frame in said incoming scalable video bit stream.

7. (Original) The apparatus as set forth in Claim 6 wherein said source type parameter indicates whether said incoming scalable video bit stream is one of a video bit stream and a film bit stream.

8. (Currently Amended) The apparatus as set forth in Claim 1 wherein said processor load controller is configured to generate at least one scale factor ~~capable of~~for controlling a level of decoding performed by said scalable video decoder.

9. (Currently Amended) A video ~~receiver~~processing system comprising:  
a buffer ~~capable of~~configured for receiving ~~and storing~~ an incoming scalable video bit stream that has been transmitted externally from a video transmitter and for storing the received bit stream;

in communicative connection with the buffer, a scalable video decoder ~~capable of~~configured for decoding ~~an incoming scalable video~~ the bit stream that has been stored and generating a baseband video signal, said scalable video decoder comprising:

an apparatus configured for controlling a processing load of said scalable video decoder comprising:

an analyzer circuit ~~capable of~~configured for measuring at least one characteristic of said ~~incoming scalable video bit stream~~ that has been stored and generating at least one video parameter associated with said at least one characteristic; and

a processor load controller ~~capable of~~configured for receiving said at least one video parameter and, in response ~~thereto~~, controlling a level of decoding of said ~~incoming scalable video bit stream~~ performed by said scalable video decoder on said bit stream that has been stored; and

~~a display coupled to said scalable video decoder, a display~~ capable of configured for displaying said baseband video signal.

10. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 9 wherein said at least one video parameter indicates a level of motion of frames in said incoming scalable video bit stream.

11. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 9 wherein said ~~wherein said~~ at least one video parameter indicates a level of detail of frames in said incoming scalable video bit stream.

12. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 9 wherein said processor load controller is further ~~capable of~~configured for receiving a frame type parameter associated with a first frame in said incoming scalable video bit stream.

13. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 12 wherein said frame type parameter comprises at least one of an I-frame parameter, a B-frame parameter, and a P-frame parameter.

14. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 13 wherein said processor load controller is further ~~capable of~~configured for receiving a source type parameter associated with said first frame in said incoming scalable video bit stream.

15. (Currently Amended) The video ~~processing system~~receiver as set forth in Claim 14 wherein said source type parameter indicates whether said incoming scalable video bit stream is one of a video bit stream and a film bit stream.

16. (Currently Amended) The video ~~receiver~~processing system as set forth in Claim 9 wherein said processor load controller is configured to ~~generates~~ at least one scale factor ~~capable of~~for controlling a level of decoding performed by said scalable video decoder.

17. (Currently Amended) A method for controlling a processing load of use with a scalable video decoder incorporated within a receiver that receives an incoming scalable video bit stream from a transmitter configured for encoding to form said incoming scalable video bit stream and further configured for transmitting said incoming scalable video bit stream~~capable of~~, said scalable video decoder being configured for decoding ~~an~~said incoming scalable video bit stream and generating a baseband video

signal, ~~asaid~~ method for controlling a processing load of the scalable video decoder comprising the steps of:

measuring, from the received bit stream, at least one characteristic of ~~the incoming scalable video~~said received bit stream;

generating at least one video parameter associated with the at least one characteristic;

in response to a value of the at least one video parameter, controlling a level of decoding of the ~~incoming scalable video~~received bit stream performed by the scalable video decoder.

18. (Original) The method as set forth in Claim 17 wherein the at least one video parameter indicates a level of motion of frames in the incoming scalable video bit stream.

19. (Currently Amended) The method as set forth in Claim 17 wherein the ~~wherein the~~ at least one video parameter indicates a level of detail of frames in the incoming scalable video bit stream.

20. (Original) The method as set forth in Claim 17 further comprising the steps of:

determining a frame type parameter associated with a first frame in the incoming scalable video bit stream;

in response to a value of the at least one frame type parameter, controlling a level of decoding of the incoming scalable video bit stream performed by the scalable video decoder.

21. (Original) The method as set forth in Claim 20 wherein the frame type parameter comprises at least one of an I-frame parameter, a B-frame parameter, and a P-frame parameter.